

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:
Adrian P. Wise et al.

Filed: February 8, 2001

For: MULTISTANDARD VIDEO
DECODER AND DECOMPRESSION
SYSTEM FOR PROCESSING
ENCODED BIT STREAMS
INCLUDING CIRCUITRY FOR
SELECTING TOKENS AND
METHODS RELATING THERETO

§ Serial No. Not Yet Known
§
§ Art Unit: 2783 (anticipated)
§
§ Examiner: Follansbee, J. (anticipated)
§
§ Attorney Docket No.:
§ 94100422(EP)USC1X1C1D11 PDDD
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PRELIMINARY AMENDMENT

Box PATENT APPLICATION
Assistant Commissioner for Patents
Washington DC 20231

FILING OF CORRESPONDENCE BY EXPRESS MAIL UNDER 37 C.F.R. § 1.10	
EL443264337US	2/8/01
Express Mail Label Number	Date of Deposit

Sir:

Prior to issuance of Serial Number 09/307,239 filed on October 7, 1997, and entitled MULTISTANDARD VIDEO DECODER AND DECOMPRESSION SYSTEM FOR PROCESSING ENCODED BIT STREAMS INCLUDING START CODES AND METHODS RELATING THERETO, applicant wishes to file a new divisional application thereon.

In the Specification:

On page 1, line 1, please delete "DATA PIPELINE SYSTEM AND DATA ENCODING METHOD" and in place thereof, please insert new title:

097933 030304
F08020 286260

--MULTISTANDARD VIDEO DECODER AND DECOMPRESSION SYSTEM
FOR PROCESSING ENCODED BIT STREAMS INCLUDING CIRCUITRY FOR
SELECTING TOKENS AND METHODS RELATING THERETO--.

On page 1, lines 2-8, please delete "This is a continuation-in-part application of U.S. Serial No. (not yet known) filed February 2, 1995, which is a continuation application of Serial No. 08/082,291 filed June 24, 1993. This application claims priority from EPO Application No. 92306038.8 filed June 30, 1992, British Application No. 9405914.4 filed March 24, 1994 and British Application No. (not yet known) filed February 28, 1995." and in place thereof please insert the following heading and paragraph:

--CROSS REFERENCE TO RELATED APPLICATIONS

This application is a divisional of U.S. Serial No. 09/307,239 filed October 7, 1997, which is a continuation of U.S. Serial No. 08/400,397 filed March 7, 1995, which is a Continuation-In-Part of U.S. Serial No. 08/382,958 filed February 2, 1995, now abandoned, which is a continuation of U.S. Serial No. 08/082,291 filed June 24, 1993, now abandoned.--

In the Claims:

Please add the following claims:

- 1 1. A decoder interface comprising:
- 2 an input circuit that has a port for receiving encoded data; and

control circuitry that is coupled to and controls the input circuit to operate selectively in a first mode to receive raw byte data at the port and in a second mode to receive tokens at the port.

2. The decoder interface of claim 1, wherein the port comprises a coded data port.

3. The decoder interface of claim 1, wherein the port comprises a microprocessor interface.

4. The decoder interface of claim 2, wherein the port further includes a microprocessor interface.

5. The decoder interface of claim 1, wherein the control circuitry includes a byte mode signal for selecting the first or the second mode.

6. The decoder interface of claim 5, wherein the mode selection is dynamically changeable.

7. The decoder interface of claim 1, wherein the received raw byte data is placed into tokens.

8. The decoder interface of claim 7, wherein a first byte of the raw byte data causes a token header to be generated.

1 9. The decoder interface of claim 8, wherein subsequent bytes of the raw
2 byte data are appended to the token header to form tokens.

1 10. A method of operating an input circuit to receive encoded data for
2 decoding purposes comprising:
3 operating the input circuit in a first mode to receive raw byte data at a port of
4 the input circuit; and
5 operating the input circuit in a second mode to receive tokens at the port of the
6 input circuit.

1 11. The method of claim 10, wherein the port is a coded data port.

1 12. The method of claim 10, wherein the port is a microprocessor interface.

1 13. The method of claim 10, wherein a byte mode selects one of the first
2 and the second modes.

1 14. The method of claim 13, wherein the mode selection is dynamically
2 changeable.

1 15. The method of claim 10, wherein operating the input circuit in the first
2 mode comprises:
3 forming tokens from the received raw byte data.

1 16. The method of claim 15, wherein forming tokens comprises:

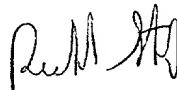
2 generating a token header in response to receiving a first byte of the raw byte
3 data.

1 17. The method of claim 16, further comprising:
2 appending subsequent bytes of the raw byte data to the generated
3 token header.

REMARKS

Should the Examiner believe that contact with Applicant's attorney would be beneficial to the disposition of this application, he is invited to contact Applicant's attorney at the telephone number listed below. The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 94-1175.

Respectfully submitted,



Date: 2/8/01

Richard Stokey
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